

Architectural Innovation for the Future of Computing



Antonio D'Acosta Rivera

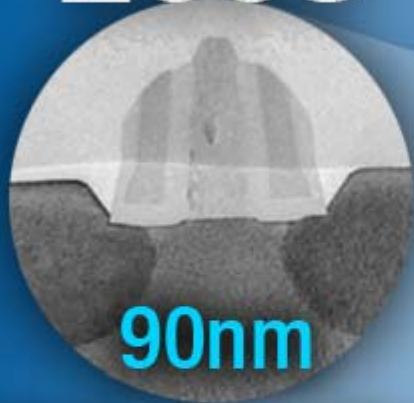
Relative Manufacturing Cost per Component



Number of Components per Integrated Circuit



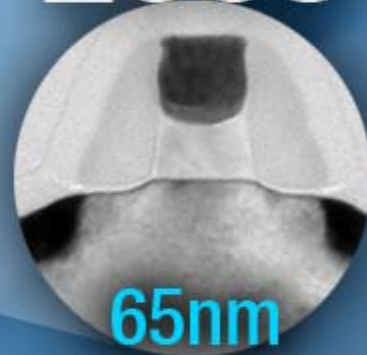
2003



90nm

**1st Generation
Strained Silicon**

2005



65nm

**2nd Generation
Strained Silicon**

Up to 30%
performance increase
Up to 5X
leakage reduction

65nm CPU Shipments



**REST of
INDUSTRY**

40,000,000

0

**8,000,000,000,000,000
transistors**



Introducing the best
microprocessor on earth...



July 27, 2006

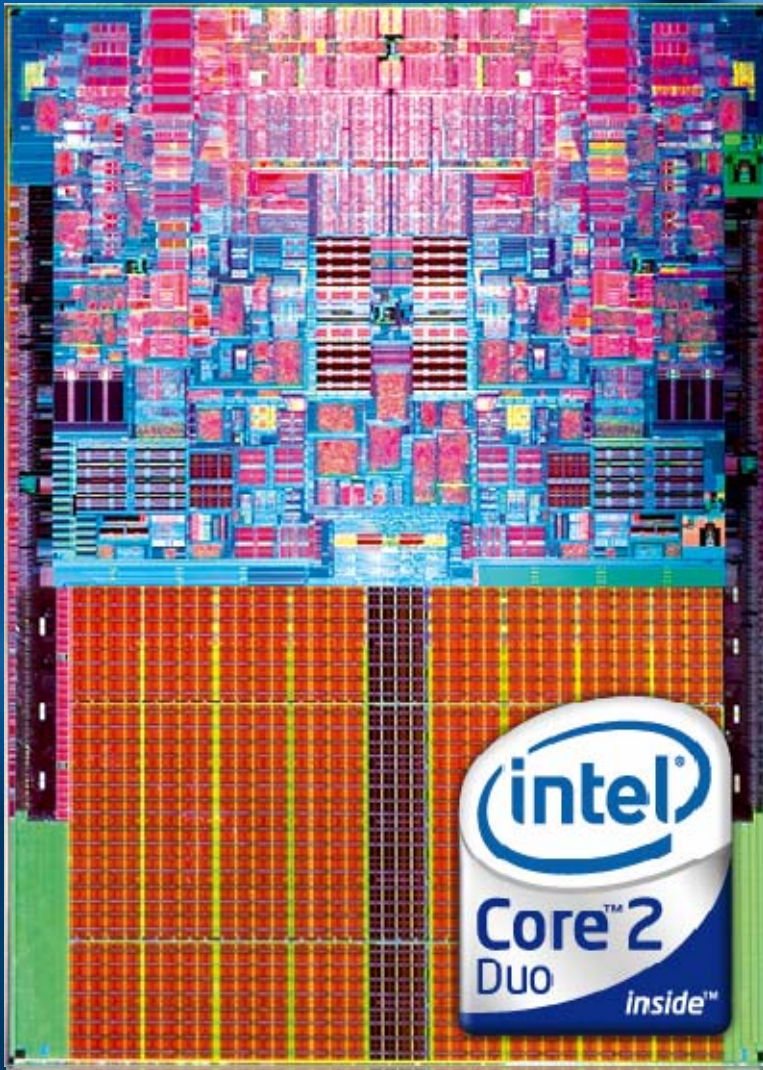


| Game Performance | Audio Performance | Video Encoding | Power Usage |
|------------------|-------------------|----------------|-------------|
| 82% | 39% | 35% | 20% |
| BETTER | BETTER | BETTER | LESS |

Core™ 2 Extreme x6800 compared to Pentium® Extreme Edition 965

“Jaw Droppingly Fast”





Over
5,000,000
in less than
60 days





Core™
Microarchitecture

SUBNOTEBOOK

5W

**THIN/LITE
NOTEBOOK**

DESKTOP

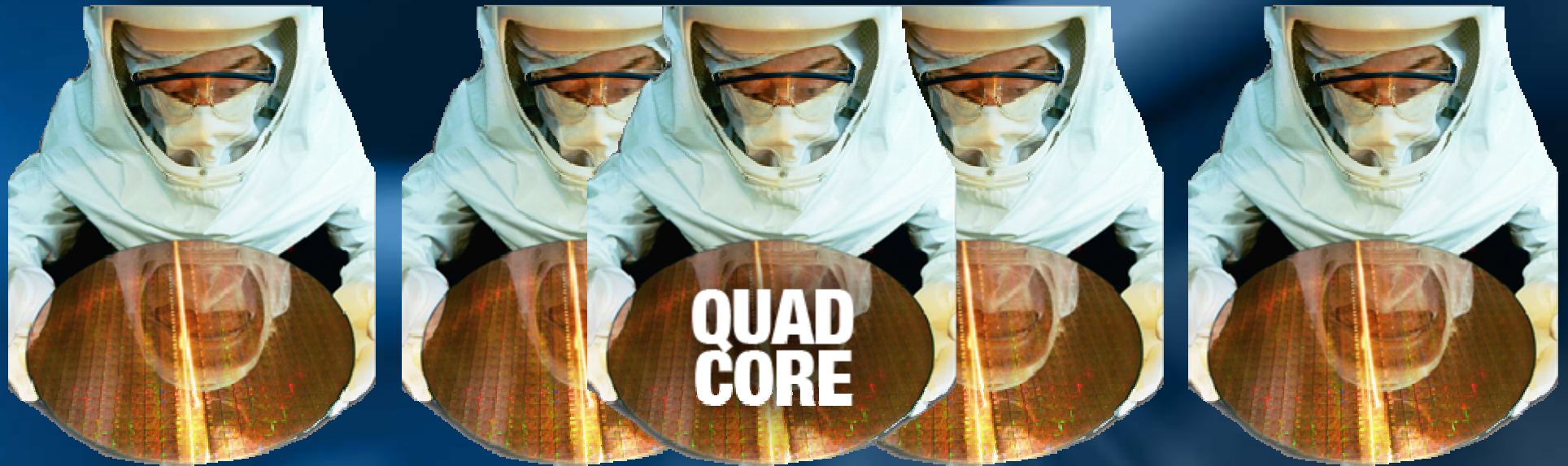
DP SERVER

95W

Delivering World's Best Performance
and Energy Efficiency



WHAT'S NEXT?



Quad-Core: Coming November 2006



“It’s like being catapulted
a year into the future”



SPECint_rate

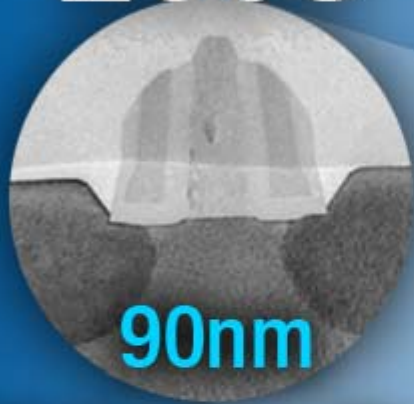
70%

BETTER

Core™ 2 Extreme QX6700 compared to Core™ 2 Extreme x6800



2003



90nm

**1st Generation
Strained Silicon**

2005



65nm

**2nd Generation
Strained Silicon**

Up to 20%
performance increase


Up to 5X
leakage reduction

2H'07



45nm





15 **45nm**
PRODUCTS in
DEVELOPMENT



World's 1st Teraflop Supercomputer

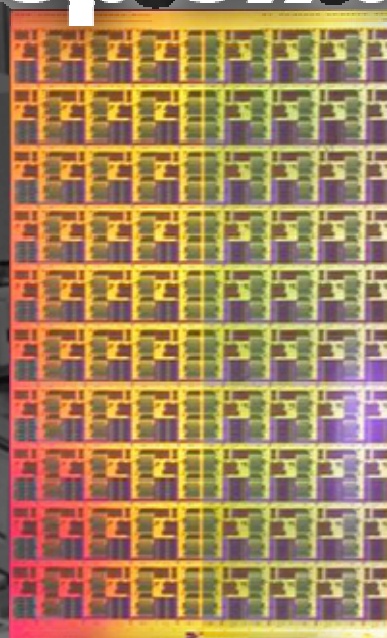
ASCI Red circa 1996



World's 1st Teraflop Supercomputer

ASCI Red circa 1996

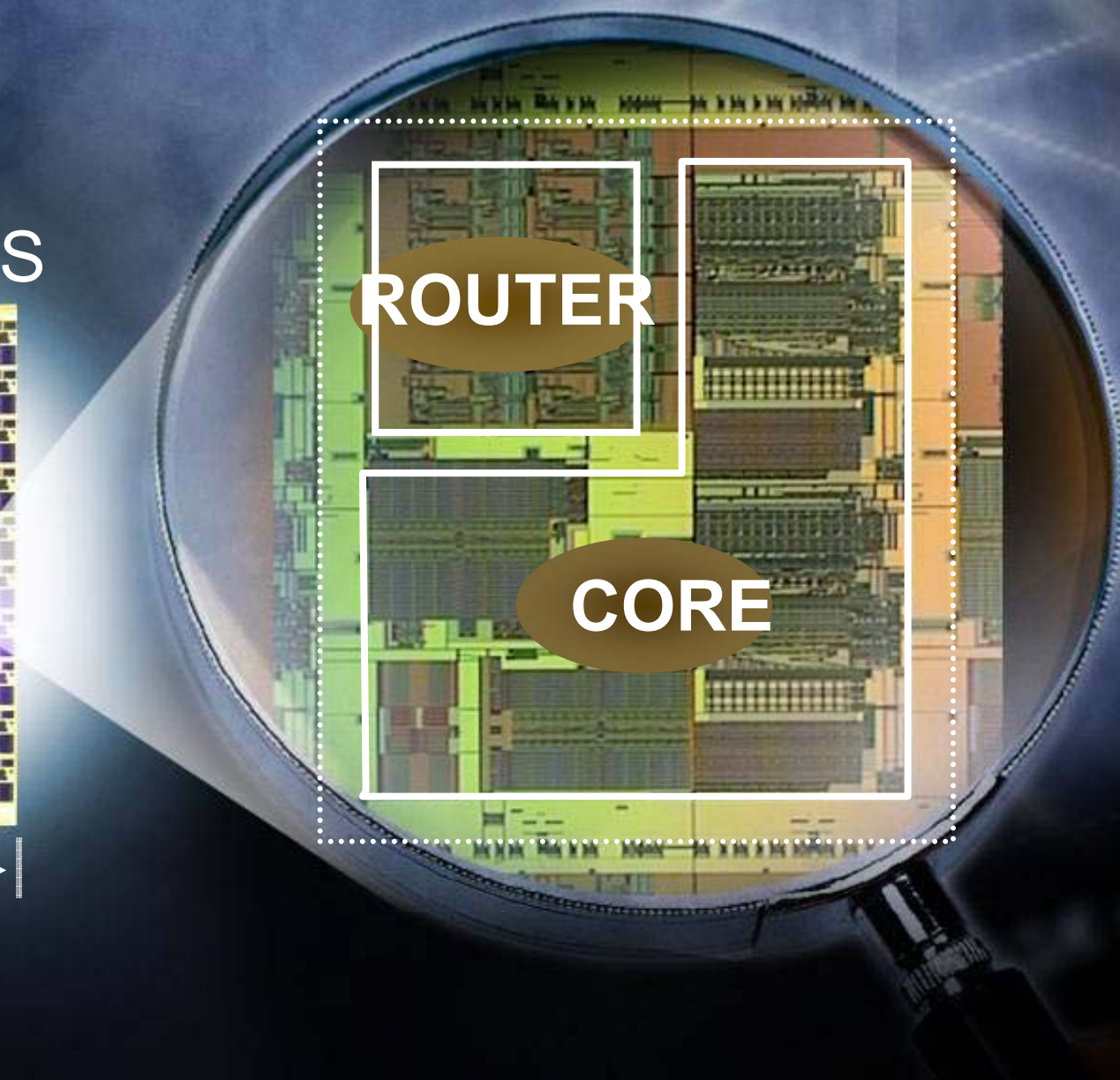
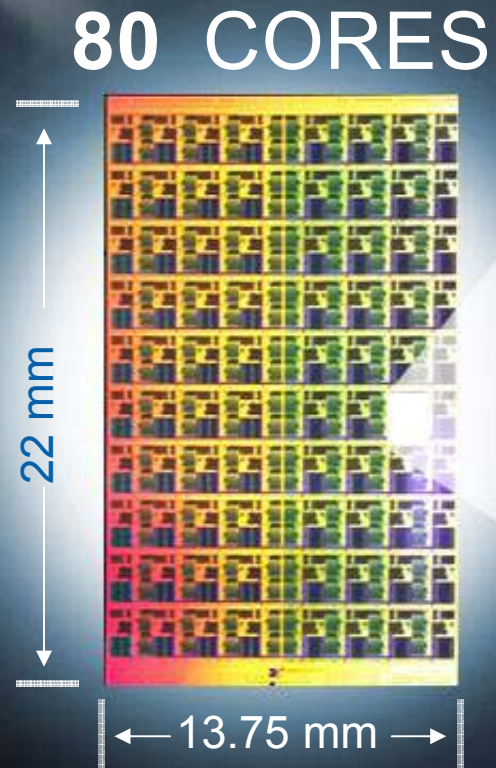
Teraflop on a Chip



2006



TERAFLOP OF PERFORMANCE



ROUTER

CORE

Grid Computing

The coordinated use of a large number of servers and storage acting as one computer

- Scalable**
- Fast – high performance**
- Fault tolerant**
- Use low-cost components**
- Provide computing power on demand**





intel[®] Leap ahead™